



## PATENT ABSTRACTS OF JAPAN

(11) Publication number: **05250992 A**(43) Date of publication of application: **28.09.93**

(51) Int. Cl. **H01J 11/02**  
**H01J 11/00**

(21) Application number: **04046478**(71) Applicant: **NEC CORP**(22) Date of filing: **04.03.92**(72) Inventor: **OKAJIMA TETSUJI****(54) PLASMA DISPLAY PANEL****(57) Abstract:**

**PURPOSE:** To form a metallic bus electrode to decrease electrical resistance of a transparent electrode easily by means of etching.

**CONSTITUTION:** An insulating layer 3 of  $\text{Al}_2\text{O}_3$  and the like is formed on a transparent electrode 2 formed of  $\text{SnO}_2$  and so on, and a metallic bus electrode 4 is formed thereon. The bus electrode 4 is formed by using a metallic thin film or thick film photolithography. Since there exists the insulating layer 3, when resist development or etching is carried out, the electrodes can be prevented from corroding due to electrochemical reaction between the transparent electrode and the metallic bus electrode.

COPYRIGHT: (C)1993,JPO&amp;Japio

